

ABSTRACT OF THE DISCLOSURE

A circuit for correcting a clock of a compact signal and a method therefor. The method comprises: receiving a data signal and a clock signal; and generating a sync pattern signal by using the clock signal to check the data signal. Then, a detection window signal according to a clock number during a timing of a last sync pattern signal and a first preset timing is generated, and the detection window signal having a second preset timing width. Finally, a clock of the sync pattern signal and the detection window signal are compared, and the clock signal is corrected.